

Title (en)

System for detecting and localising impedance error adjustments

Title (de)

System zur Erkennung und Lokalisierung von Impedanzfehlanspassungen

Title (fr)

Système de reconnaissance et de localisation d'adaptations erronées d'impédance

Publication

**EP 2328288 B1 20201014 (DE)**

Application

**EP 10015153 A 20101130**

Priority

ES 200902278 A 20091130

Abstract (en)

[origin: EP2328288A1] The system has a power detector (302) for informing a microprocessor-controller (308) that high-frequency signals are present at an input. A local oscillator (305) and a mixer (303) are provided for converting the signals into intermediate frequency, where the system is designed in such a manner that the signals are input signals in a satellite master antenna TV-network. The microprocessor-controller detects the position of impedance-error adjustments in the network. A user interface (309) is provided for communicating a user with the microprocessor-controller. An independent claim is also included for a method for detecting and localizing impedance error adjustments in a satellite master antenna TV-network.

IPC 8 full level

**H04H 20/12** (2008.01); **H04H 20/63** (2008.01)

CPC (source: EP ES)

**H04H 20/12** (2013.01 - EP ES); **H04H 20/63** (2013.01 - EP)

Cited by

CN117706255A

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)

**EP 2328288 A1 20110601**; **EP 2328288 B1 20201014**; ES 2380532 A1 20120516; ES 2826987 T3 20210519; PL 2328288 T3 20210419; PT 2328288 T 20201207

DOCDB simple family (application)

**EP 10015153 A 20101130**; ES 10015153 T 20101130; ES 200902278 A 20091130; PL 10015153 T 20101130; PT 10015153 T 20101130