

Title (en)
CIRCUIT AND METHOD FOR THE PRODUCTION THEREOF

Title (de)
SCHALTUNG UND VERFAHREN ZU DEREN HERSTELLUNG

Title (fr)
CIRCUIT ET PROCEDE DE FABRICATION DUDIT CIRCUIT

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Application
EP 09778417 A 20090909

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Abstract (en)
[origin: WO2010034404A2] The invention relates to a circuit comprising a substrate of a semiconductor module, having at least one power capacitor disposed on the substrate, wherein the semiconductor module comprises a power stage and a logic circuit. The invention further relates to a method for producing said circuit.

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See references of WO 2010034404A2

Citation (examination)
DE 10029122 A1 20010705 - MITSUBISHI ELECTRIC CORP [JP]

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