

Title (en)
METHOD FOR CONTROLLING THE LOOP DELAY IN A SIGMA-DELTA MODULATOR, AND SIGMA-DELTA MODULATOR IMPLEMENTING SAID METHOD

Title (de)
VERFAHREN ZUR SCHLEIFENVERZÖGERUNGSSTEUERUNG BEI EINEM SIGMA-DELTA-MODULATOR UND SIGMA-DELTA-MODULATOR MIT DIESEM VERFAHREN

Title (fr)
PROCEDE DE CONTROLE DU RETARD DE BOUCLE DANS UN MODULATEUR SIGMA-DELTA ET MODULATEUR METTANT EN UVRE LE PROCEDE

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Application
EP 09756277 A 20091029

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Abstract (en)
[origin: WO2010049504A1] The invention relates to a method for controlling the loop delay in a sigma-delta modulator that consists of a loop comprising at least one integrator (400, 500), an analog-to-digital converter ADC (401, 501), a digital-to-analog converter DAC (402, 502) and an adder-subtractor (403, 503), characterised in that at least one phase control programmable digital control (f1, f2) representing an offset is applied to one of the clock signals of the loop converters in order to adjust the relative phase between the clock signal h1(t) of the ADC converter (401, 501) and the clock signal h2(t) of the DAC converter (402, 502). The invention also relates to a sigma-delta modulator for implementing said method.

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