

Title (en)
A DISPLAY DEVICE

Title (de)
ANZEIGEEINRICHTUNG

Title (fr)
DISPOSITIF D'AFFICHAGE

Publication
EP 2351007 A4 20121205 (EN)

Application
EP 08748803 A 20080522

Priority
DK 2008000192 W 20080522

Abstract (en)
[origin: WO2009140963A1] A display system with a display control circuit (105, 200), one or more display panels (104) and configuration circuitry (106) for controlling the display control circuitry (105) and display panels (104). The display control circuitry (105, 200) receives and processes digital video signals via a serial interface (201) e.g. Mobile Display Digital Interface Standard, MDDI. The digital video signals are unpacked in the pre-processor (202), stored in the frame buffer (203) and transmitted to the display panel via the postprocessor (204) and serial interface (201), the pre-processor (202), frame buffer (203) and postprocessor (204) is controlled by the central control (205). The display system further provides a clock signal for driving the display panels, which phase is adjusted according to an internal clock signal. The packet structure of MDDI is also disclosed.

IPC 8 full level
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CPC (source: EP)
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Citation (search report)

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- See references of WO 2009140963A1

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