

Title (en)

SYSTEM AND METHOD OF DETECTING AND LOCATING INTERMITTENT ELECTRICAL FAULTS IN ELECTRICAL SYSTEMS

Title (de)

SYSTEM UND VERFAHREN ZUM DETEKTIEREN UND LOKALISIEREN VON IN ABSTÄNDEN AUFTRETENDEN ELEKTRISCHEN FEHLERN IN ELEKTRISCHEN SYSTEMEN

Title (fr)

SYSTÈME ET PROCÉDÉ DE DÉTECTION ET DE LOCALISATION DE DÉFAUTS ÉLECTRIQUES INTERMITTENTS DANS DES SYSTÈMES ÉLECTRIQUES

Publication

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Application

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Abstract (en)

[origin: WO2010051221A1] A fault determination apparatus includes a housing. The housing includes a circuit board disposed there within. The housing is shaped and dimensioned for non-intrusive placement within an existing electrical system. The circuit board is configured to provide one of a transmitter or a receiver within a fault determination system. A first set of connectors is disposed at a first side of the housing and a second set of connectors disposed at the second end of the housing. The first set of connectors and the second set of connectors are directly connected through the wires provided inside the apparatus while at least one wires under fault monitoring are tapped to the circuit board and configured so as to be electrically coupled to the existing electrical system.

IPC 8 full level

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