Title (en)

Macro function block for encapsulating device-level embedded logic

Title (de)

Makrofunktionsblock zum Verkapseln einer eingebetteten Logik auf Vorrichtungsebene

Title (fr)

Macro de blocs fonctionnels pour l?encapsulation de logique intégrée au niveau équipement

Publication

EP 2357541 A1 20110817 (EN)

Application

EP 11154335 A 20110214

Priority
- US 30422710 P 20100212
- US 30426110 P 20100212
- US 30427510 P 20100212
- US 89539210 A 20100930

Abstract (en)

The present invention provides techniques for encapsulating device-level embedded logic into user-defined instructions using configuration software. More specifically, the disclosed embodiments enable add-on instructions for device configuration embedded logic. Programming interfaces specific to an application or device may be combined into a single instruction as a reusable Macro component that may be reused in the same or different applications or devices.

IPC 8 full level

G05B 19/042 (2006.01); **G05B 19/045** (2006.01); **G05B 19/05** (2006.01)

CPC (source: EP US)

G05B 19/0426 (2013.01 - EP US); **G05B 19/045** (2013.01 - EP US); **G05B 19/056** (2013.01 - EP US); **G06F 8/34** (2013.01 - EP);
G05B 2219/23258 (2013.01 - EP US); G05B 2219/23274 (2013.01 - EP US); G05B 2219/25232 (2013.01 - EP US)

Citation (applicant)
- US 61304227 A
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- US 61304275 A

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- [X] GB 2456037 A 20090701 - FISHER ROSEMOUNT SYSTEMS INC [US]
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Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

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DOCDB simple family (application)

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