

Title (en)

CONTROLLER FOR SOLID STATE DISK WHICH CONTROLS ACCESS TO MEMORY BANK

Title (de)

STEUERUNG FÜR SOLID-STATE-DISKS, DIE ZUGRIFF AUF SPEICHERBANK STEUERT

Title (fr)

CONTRÔLEUR POUR DISQUE À CIRCUITS INTÉGRÉS COMMANDANT L'ACCÈS À UNE BANQUE DE MÉMOIRE

Publication

EP 2367175 A4 20121128 (EN)

Application

EP 09826235 A 20091030

Priority

- KR 2009006337 W 20091030
- KR 20080112575 A 20081113

Abstract (en)

[origin: EP2367175A2] A controller for a solid state disk is provided. The controller includes a storage module to store an index of at least one idle bank among a plurality of memory banks, and a control module to control an access to the at least one idle bank using the stored index. Here, the access to the at least one idle bank may be controlled based on a state of a channel corresponding to each of the at least one idle bank.

IPC 8 full level

G06F 13/38 (2006.01)

CPC (source: EP KR US)

G06F 13/1668 (2013.01 - KR); **G06F 13/385** (2013.01 - EP KR US); **G06F 2212/214** (2013.01 - KR); **G06F 2212/7211** (2013.01 - KR)

Citation (search report)

- [A] WO 2005098623 A2 20051020 - EMULEX DESIGN & MFG CORP [US], et al
- [A] WO 0028418 A1 20000518 - INTEL CORP [US]
- [T] US 2009292865 A1 20091126 - HONG DU-WON [KR]
- [I] KANG ET AL: "A multi-channel architecture for high-performance NAND flash-based storage system", JOURNAL OF SYSTEMS ARCHITECTURE, ELSEVIER BV, NL, vol. 53, no. 9, 16 May 2007 (2007-05-16), pages 644 - 658, XP022081344, ISSN: 1383-7621, DOI: 10.1016/J.SYNSARC.2007.01.010
- [A] YU-BIN CHANG ET AL: "A Self-Balancing Striping Scheme for NAND-Flash Storage Systems", SAC'08, 16 March 2008 (2008-03-16), Fortaleza, Cear'a, Brazil, XP055041505
- See references of WO 2010056003A2

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Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO SE SI SK SM TR

DOCDB simple family (publication)

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