

Title (en)
SOLAR CELL AND METHOD FOR PRODUCING A SOLAR CELL FROM A SILICON SUBSTRATE

Title (de)
SOLARZELLE UND VERFAHREN ZUR HERSTELLUNG EINER SOLARZELLE AUS EINEM SILIZIUMSUBSTRAT

Title (fr)
CELLULE SOLAIRE ET PROCÉDÉ DE FABRICATION D'UNE CELLULE SOLAIRE À PARTIR D'UN SUBSTRAT DE SILICIUM

Publication
EP 2377169 A2 20111019 (DE)

Application
EP 09771303 A 20091203

Priority
• EP 2009008605 W 20091203
• DE 102009005168 A 20090114

Abstract (en)
[origin: WO2010081505A2] The invention relates to a method for producing a solar cell having a front and a rear side from a silicon substrate (1), in particular a silicon wafer, comprising the following process steps: A) texturizing at least one side of the silicon substrate (1) for improving the absorption when the solar cell is exposed to electromagnetic radiation and/or removing saw damage at at least one side of the silicon substrate (1); B) generating at least one emitter area (2) at least at partial areas of at least one side of the silicon substrate (1) by diffusing in at least one doping material for forming at least one pn transition; C) removing a glass layer on at least one side of the silicon substrate (1), wherein the glass substrate comprises the doping material; D) applying a masking layer (3) at least on a partial area of at least one side of the silicon substrate (1), wherein the masking layer (3) is a dielectric layer; E) removing at least one part of the material of the silicon substrate (1) at at least one side of the silicon substrate (1) and/or conditioning at least one side of the silicon substrate (1); F) applying metal structures (5, 6) to the front side (1a) and/or rear side (1b) of the silicon substrate (1) for electrically contacting the solar cell. It is significant that thermal oxidation is performed in a process step E2 between the process steps E and F for forming an oxide layer (4) at least in a partial area of the front and/or rear side of the silicon substrate (1) not covered by the masking layer (3) applied in step D, and that the marking layer (3) and the oxide layer (4) substantially remain on the silicon substrate (1) in the subsequent process steps.

IPC 8 full level
H01L 31/18 (2006.01); **H01L 31/0224** (2006.01); **H01L 31/068** (2012.01)

CPC (source: EP US)
H01L 31/02245 (2013.01 - EP US); **H01L 31/068** (2013.01 - EP US); **H01L 31/1868** (2013.01 - EP US); **Y02E 10/547** (2013.01 - EP US); **Y02P 70/50** (2015.11 - EP US)

Citation (search report)
See references of WO 2010081505A2

Designated contracting state (EPC)
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO SE SI SK SM TR

DOCDB simple family (publication)
DE 102009005168 A1 20100722; CN 102282683 A 20111214; EP 2377169 A2 20111019; US 2011272020 A1 20111110;
WO 2010081505 A2 20100722; WO 2010081505 A3 20110414

DOCDB simple family (application)
DE 102009005168 A 20090114; CN 200980154540 A 20091203; EP 09771303 A 20091203; EP 2009008605 W 20091203;
US 200913144531 A 20091203