

Title (en)
CIRCUITS FOR SOFT LOGICAL FUNCTIONS

Title (de)
SCHALTKREISE FÜR WEICHE LOGIKFUNKTIONEN

Title (fr)
CIRCUITS POUR FONCTIONS LOGIQUES PROGRAMMABLES

Publication
EP 2404415 A1 20120111 (EN)

Application
EP 10749220 A 20100302

Priority
• US 2010025950 W 20100302
• US 15679409 P 20090302
• US 15673509 P 20090302

Abstract (en)
[origin: WO2010101941A1] A circuit implementing a soft logical processing network includes an interconnection of analog processing elements, which can include soft logic gates, for instance soft Equals gates and soft XOR gates. In some examples, each of the soft logic gates include multiple circuit parts, with each part including an input configured to accept a voltage signal representation of a soft logical quantity, and a conversion section configured to use the accepted voltage representation to form a corresponding current signal. The current signals are combined to form a signal representation of the output of the gate. In an application of soft logic gates, a memory includes a group of electrical storage elements, each electrical storage elements carrying a respective storage values; a group of conversion elements, each conversion element being coupled to a respective electrical storage element for selectively converting the corresponding storage value to a current signal; and a current combination element for combining the current signals to form an output signal.

IPC 8 full level
H04L 27/06 (2006.01); **G06G 7/14** (2006.01); **G06G 7/52** (2006.01); **H03K 19/173** (2006.01)

CPC (source: EP)
G06G 7/122 (2013.01); **G06G 7/14** (2013.01); **H03K 19/1733** (2013.01)

Designated contracting state (EPC)
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO SE SI SK SM TR

DOCDB simple family (publication)
WO 2010101941 A1 20100910; CN 102449969 A 20120509; CN 102449969 B 20150701; EP 2404415 A1 20120111; EP 2404415 A4 20150805

DOCDB simple family (application)
US 2010025950 W 20100302; CN 201080018700 A 20100302; EP 10749220 A 20100302