

Title (en)

ANR CIRCUIT WITH EXTERNAL CIRCUIT COOPERATION

Title (de)

ANR-SCHALTUNG MIT KOOPERATION EXTERNER SCHALTUNG

Title (fr)

CIRCUIT ANR AVEC COOPÉRATION DE CIRCUIT EXTERNE

Publication

**EP 2425422 A1 20120307 (EN)**

Application

**EP 10715643 A 20100426**

Priority

- US 2010032353 W 20100426
- US 43097609 A 20090428
- US 43100009 A 20090428

Abstract (en)

[origin: WO2010129212A1] At initialization of an ANR circuit (i.e., at so-called boot time), attempts are made to alternately obtain ANR settings from an external storage device by operating the ANR circuit as a master of a bus and from an external processing device by operating the ANR circuit as a slave of the bus. Such alternating attempts may be repeated until the ANR settings are loaded. In a personal ANR device having a pair of earpieces, each having a feedforward microphone and an ANR circuit associated therewith, each ANR circuit provides digital data representing environmental noise sounds detected by each ANR circuit's associated feedforward microphone. Such digital data may represent the environmental noise sounds as detected with little or not modification, or such digital data may be modified to some degree within each ANR circuit, perhaps to limit the range of frequencies that such digital data represents, before being provided to the other ANR circuit.

IPC 8 full level

**G10K 11/178** (2006.01)

CPC (source: EP US)

**G10K 11/17823** (2017.12 - EP US); **G10K 11/17827** (2017.12 - EP US); **G10K 11/17833** (2017.12 - EP US); **G10K 11/17855** (2017.12 - EP US);  
**G10K 11/17857** (2017.12 - EP US); **G10K 11/17881** (2017.12 - EP US); **G10K 11/17885** (2017.12 - EP US); **G10K 2210/321** (2013.01 - EP);  
**G10K 2210/3214** (2013.01 - EP)

Citation (search report)

See references of WO 2010129212A1

Designated contracting state (EPC)

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DOCDB simple family (publication)

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DOCDB simple family (application)

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