

Title (en)
Interference-tolerant communication circuit

Title (de)
Störungstolerante Kommunikationsschaltung

Title (fr)
Circuit de communication tolérant les interférences

Publication
EP 2453620 A3 20150506 (EN)

Application
EP 11188875 A 20111111

Priority
US 94524810 A 20101112

Abstract (en)
[origin: EP2453620A2] An interference-tolerant transmitter is provided. In accordance with various example embodiments, a transmitter circuit includes a control circuit configured to maintain the sum of current as applied to a load from respective high-side and low-side current sources at a target level (e.g ., range). In some applications, clamp circuits are used to clamp current to high and low sides of the load respectively in response to changes at the low-side and high-side of the load.

IPC 8 full level
H04L 25/08 (2006.01); **H04L 25/02** (2006.01)

CPC (source: EP US)
H04L 25/028 (2013.01 - EP US); **H04L 25/08** (2013.01 - EP US)

Citation (search report)

- [X] US 2010079186 A1 20100401 - ZANNOTH MARKUS [DE], et al
- [A] US 2003122593 A1 20030703 - VOLK ANDREW M [US]
- [A] EP 1376962 A1 20040102 - MOTOROLA INC [US]

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)
BA ME

DOCDB simple family (publication)
EP 2453620 A2 20120516; EP 2453620 A3 20150506; EP 2453620 B1 20160831; US 2012119793 A1 20120517; US 8368432 B2 20130205

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