

Title (en)

Reduction of the sensitivity of jitter demodulation of a sampling clock signal

Title (de)

Verringerung der Sensitivität der Jitterdemodulation eines Abtasttaktsignals

Title (fr)

Réduction de la sensibilité de la démodulation à la gigue du signal d'horloge d'échantillonnage

Publication

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Application

EP 10736756 A 20100713

Priority

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Abstract (en)

[origin: WO2011007057A1] The invention relates to a method for the demodulation of a radiofrequency signal (Y), that comprises the steps of: providing a synchronous sampling clock signal (HE) of said radiofrequency signal to be demodulated; sampling said radiofrequency signal using said sampling clock signal; and processing the samples thus obtained in order to determine the phase and/or amplitude of said radiofrequency signal; characterized in that it further comprises the step of adjusting the phase, as measured relative to the sampling clock signal, of said signal to be demodulated and/or of a synchronous reference signal (R) relative to which the signal is demodulated in order to minimize the phase and/or amplitude error generated by a jitter of said sampling clock signal. The invention also relates to a demodulator circuit for implementing said method.

IPC 8 full level

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