

Title (en)
Differential microphone circuit

Title (de)
Differentialmikrofonschaltung

Title (fr)
Circuit de microphone différentiel

Publication
EP 2461605 A1 20120606 (EN)

Application
EP 10193886 A 20101206

Priority
EP 10193886 A 20101206

Abstract (en)
There is provided an electret microphone comprising a junction gate-field-effect transistor (JFET) and a bias resistor connected to the JFET and supplying current to the JFET, whereby an electrical output is determined by measuring a differential voltage across the bias resistor.

IPC 8 full level
H04R 19/01 (2006.01)

CPC (source: EP)
H04R 19/016 (2013.01)

Citation (search report)
• [IY] US 5097224 A 19920317 - MADAFFARI PETER L [US], et al
• [Y] WO 2005101902 A1 20051027 - BSE CO LTD [KR], et al
• [Y] EP 0055650 A1 19820707 - THOMSON CSF MAT TEL [FR]
• [Y] EP 1748676 A2 20070131 - HOSIDEN CORP [JP]
• [A] US 5589799 A 19961231 - MADAFFARI PETER L [US], et al
• [Y] NÜHRMANN: "Transistor-Praxis: Berechnungen, Grundsaltungen, Applikationen", 31 December 1986, FRANZIS, Munich, ISBN: 3772381316, pages: 180 - 180, XP002638525

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)
BA ME

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DOCDB simple family (application)
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