

Title (en)

Methods and system for verifying memory device integrity

Title (de)

Verfahren und System zur Verifizierung der Integrität einer Speichervorrichtung

Title (fr)

Procédés et système de vérification d'intégrité de dispositif de mémoire

Publication

**EP 2469412 B1 20180829 (EN)**

Application

**EP 11194220 A 20111219**

Priority

US 97458710 A 20101221

Abstract (en)

[origin: EP2469412A1] A method for validating an eligibility for verification of a memory device (110) within an embedded demand paged memory operating system environment is provided. The method includes receiving a request from an application being executed by a processor (115) coupled to the memory device (110), the request to utilize at least one memory location (150). The method includes identifying, by the processor (115), at least one memory block (160) corresponding to at least one memory location (150) within the memory device (110), determining, by the processor (115), whether the at least one memory block (160) is eligible for verification, and producing an eligibility result based on the determination by the processor (115). A system for validating an eligibility for verifying memory device (110) integrity is also disclosed.

IPC 8 full level

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US 6678837 B1 20040113 - QUACH NHON [US], et al

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