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Publication  
**EP 2471168 A2 20120704 (EN)**

Application  
**EP 10760750 A 20100831**

Priority  
• GB 0915070 A 20090829  
• GB 2010051427 W 20100831

Abstract (en)  
[origin: WO2011024010A2] A power inverter arranged to receive input power having an input voltage across first and second input terminals, comprises a first switching arm, a second switching arm, and a voltage divider coupled across the input terminals. The first switching arm comprises first and second switching elements. The second switching arm comprises third and fourth switching elements. The first and second switching arms are connected in series across the input terminals. Drive circuitry drives each of the switching elements to produce switching waveforms such that the voltage across each of the switching arms remains below a predetermined proportion of the input voltage.

IPC 8 full level  
**H02M 3/335** (2006.01); **H02M 7/487** (2007.01)

CPC (source: EP GB US)  
**H02M 1/32** (2013.01 - GB); **H02M 3/335** (2013.01 - GB); **H02M 3/33573** (2021.05 - EP GB US); **H02M 7/487** (2013.01 - EP US);  
**H02M 7/53** (2013.01 - GB); **H02M 1/0074** (2021.05 - EP); **H02M 1/0077** (2021.05 - EP)

Citation (search report)  
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