Title (en)

PIXEL CIRCUIT AND DISPLAY DEVICE

Title (de)

PIXELSCHALTUNG UND ANZEIGEVORRICHTUNG

Title (fr)

CIRCUIT DE PIXEL ET DISPOSITIF D'AFFICHAGE

Publication

EP 2477180 A1 20120718 (EN)

Application

EP 10813550 A 20100524

Priority

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- JP 2010058743 W 20100524

Abstract (en)

A display device that realizes a reduction in power consumption without causing deterioration of an aperture is provided. A liquid crystal capacitor element (Clc) is interposed between a pixel electrode (20) and a counter electrode (80). A counter voltage (Vcom) is applied to the counter electrode (80). The pixel electrode (20), one terminals of a first switch circuit (22) and a second switch circuit (23), and a first terminal of a second transistor (T2) form an internal node (N1). The other terminals of the first switch circuit (22) is connected to the source line (SL). The other terminal of the second switch circuit (23) is connected to the voltage supply line (VSL) and is configured by a series circuit of transistors (T1 and T3). A control terminal of the transistor (T1), a second terminal of the transistor (T2), and one terminal of a boost capacitor element (Cbst) form an output node (N2). The other terminal of the boost capacitor element (Csbt), the control terminal of the transistor (T2), and the control terminal of the transistor (T3) are connected to a boost line (BST), a reference line (REF), and a selecting line (SEL), respectively.

IPC 8 full level

G09G 3/36 (2006.01); **G02F 1/133** (2006.01); **G02F 1/1368** (2006.01); **G09G 3/20** (2006.01)

CPC (source: EP US)

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