

Title (en)
AN APPARATUS

Title (de)
VORRICHTUNG

Title (fr)
APPAREIL

Publication
EP 2484127 A4 20130619 (EN)

Application
EP 10819956 A 20100915

Priority
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Abstract (en)
[origin: WO2011039413A1] An apparatus comprising at least one processor and at least one memory including computer program code. The at least one memory and the computer program code is configured to, with the at least one processor, cause the apparatus at least to perform filtering at least two audio signals to generate at least two groups of audio components per audio signal, determining a difference between the at least two audio signals for each group of audio components, and generating a further audio signal by selectively combining the at least two audio signals for each group of audio components dependent on the difference between the at least two audio signals for each group of audio components.

IPC 8 full level
H04S 1/00 (2006.01); **H04S 3/00** (2006.01); **H04S 5/00** (2006.01)

CPC (source: EP)
H04S 5/00 (2013.01); **H04S 2400/05** (2013.01); **H04S 2420/01** (2013.01); **H04S 2420/03** (2013.01); **H04S 2420/07** (2013.01)

Citation (search report)
• [XA] EP 1784048 A2 20070509 - SONY CORP [JP]
• [X] US 2005169482 A1 20050804 - REAMS ROBERT [US], et al
• See references of WO 2011039413A1

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO SE SI SK SM TR

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DOCDB simple family (application)
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