

Title (en)  
MEMORY ARRAY

Title (de)  
SPEICHERANORDNUNG

Title (fr)  
ENSEMBLE MÉMOIRE

Publication  
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Application  
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Abstract (en)  
[origin: WO2011054641A1] The invention relates to a memory array having a memory (101), wherein at least one memory region (201, 203) of the memory (101) can be configured depending on a required memory operating speed as a data memory or buffer memory.

IPC 8 full level  
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