

Title (en)
SHIFT REGISTER AND DISPLAY APPARATUS

Title (de)
SCHIEBEREGISTER UND ANZEIGEVORRICHTUNG

Title (fr)
REGISTRE À DÉCALAGE ET APPAREIL D'AFFICHAGE

Publication
EP 2506306 A4 20171011 (EN)

Application
EP 10832902 A 20100709

Priority
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Abstract (en)
[origin: EP2506306A1] The present invention provides a shift register and a display device, each of which operates stably. The present invention relate to a shift register, comprising a thin-film transistor which includes a source electrode, a drain electrode, and a gate electrode, the thin-film transistor being a bottom gate thin-film transistor which includes a comb-shaped source/drain structure, the gate electrode being provided with at least one of a cut and an opening in at least one of a region overlapping with the source electrode and a region overlapping with the drain electrode.

IPC 8 full level
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CPC (source: EP US)
G09G 3/3677 (2013.01 - EP US); **G11C 19/184** (2013.01 - EP US); **G11C 19/28** (2013.01 - EP US); **H01L 27/124** (2013.01 - EP US); **H01L 29/41733** (2013.01 - EP US); **H01L 29/42384** (2013.01 - EP US); **G09G 3/3266** (2013.01 - EP US); **G09G 2300/0408** (2013.01 - EP US); **G09G 2310/0286** (2013.01 - EP US)

Citation (search report)
• [X1] US 2004173795 A1 20040909 - MOON SEUNG-HWAN [KR], et al
• [X1] US 2006049404 A1 20060309 - PARK HAENG-WON [KR], et al
• [X1] WO 2009104302 A1 20090827 - SHARP KK [JP], et al
• [A] FR 2810148 A1 20011214 - LG PHILIPS LCD CO LTD [KR]
• See references of WO 2011065055A1

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO SE SI SK SM TR

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