

Title (en)

Over-temperature protected triac and protection method

Title (de)

Übertemperaturgeschützter Triac und Schutzverfahren

Title (fr)

Triac protégé contre la surchauffe et procédé de protection

Publication

**EP 2506435 A1 20121003 (EN)**

Application

**EP 11160706 A 20110331**

Priority

EP 11160706 A 20110331

Abstract (en)

A triac circuit comprises a triac having first and second main terminals (MT1,MT2) and a gate terminal and a thyristor connected between one of the main terminals (MT1,MT2) and a control terminal of the triac circuit. The thyristor is used to prevent turn on of the triac when it has turned on by temperature induced leakage currents.

IPC 8 full level

**H03K 17/725** (2006.01)

CPC (source: EP US)

**H03K 17/725** (2013.01 - EP US); **H01L 2224/0603** (2013.01 - EP US); **H01L 2224/06181** (2013.01 - EP); **H01L 2224/4813** (2013.01 - EP US); **H01L 2224/48247** (2013.01 - EP US); **H01L 2224/48472** (2013.01 - EP US); **H01L 2224/4903** (2013.01 - EP US); **H01L 2224/49111** (2013.01 - EP US)

Citation (search report)

- [X] US 4087848 A 19780502 - HYINK ROY, et al
- [XI] US 3920955 A 19751118 - NAKATA JOSUKE
- [A] US 3971056 A 19760720 - JASKOLSKI STANLEY V, et al
- [A] US 4142115 A 19790227 - NAKATA JOSUKE, et al

Cited by

CN110932240A; CN110745118A; CN106960809A

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

**EP 2506435 A1 20121003**; **EP 2506435 B1 20160518**; CN 102739221 A 20121017; CN 102739221 B 20150204; US 2012250200 A1 20121004; US 8630074 B2 20140114

DOCDB simple family (application)

**EP 11160706 A 20110331**; CN 201210088367 A 20120328; US 201213433582 A 20120329