

Title (en)
LIMITING CIRCUIT

Title (de)
BEGRENZERSCHALTUNG

Title (fr)
CIRCUIT LIMITEUR

Publication
EP 2510617 A1 20121017 (DE)

Application
EP 10807515 A 20101125

Priority
• DE 102009057544 A 20091209
• DE 2010001387 W 20101125

Abstract (en)
[origin: WO2011069479A1] The invention relates to a limiting circuit having a signal input (E1) and a signal output (A) for limiting an output signal that is present at the signal output (A) and that can be fed to a further circuit (SCH) connected to the output of the limiting circuit, wherein a voltage connection (E2) for feeding a bias voltage (U) and a transistor (T) are present, wherein the gate connection (G) of the transistor (T) is connected to the voltage connection (E2) by means of a first matching circuit (A1) and to the signal input (E1) by means of a second matching circuit (A2).

IPC 8 full level
H03F 1/52 (2006.01); **H03G 11/04** (2006.01); **H04B 1/18** (2006.01)

CPC (source: EP US)
H03F 1/523 (2013.01 - EP US); **H03G 11/04** (2013.01 - EP US); **H03F 2200/211** (2013.01 - EP US); **H03F 2200/441** (2013.01 - EP US); **H03F 2200/66** (2013.01 - EP US)

Citation (search report)
See references of WO 2011069479A1

Citation (examination)
• US 5301081 A 19940405 - PODELL ALLEN F [US], et al
• EP 1480332 A2 20041124 - SAMSUNG ELECTRONICS CO LTD [KR]

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DOCDB simple family (application)
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