

Title (en)
FINE GRAIN PERFORMANCE RESOURCE MANAGEMENT OF COMPUTER SYSTEMS

Title (de)
FEINKÖRNIGES LEISTUNGSRESSOURCENMANAGEMENT VON COMPUTERSYSTEMEN

Title (fr)
GESTION DE RESSOURCES DE PERFORMANCES DE GRAIN FIN POUR SYSTÈMES INFORMATIQUES

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Application
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Priority

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- US 2011030096 W 20110325

Abstract (en)
[origin: WO2011120019A2] Execution of a plurality of tasks by a processor system are monitored. Based on this monitoring, tasks requiring adjustment of performance resources are identified by calculating at least one of a progress error or a progress limit error for each task. Thereafter, performance resources of the processor system allocated to each identified task are adjusted. Such adjustment can comprise: adjusting a clock rate of at least one processor in the processor system executing the task, adjusting an amount of cache and/or buffers to be utilized by the task, and/or adjusting an amount of input/output (I/O) bandwidth to be utilized by the task. Related systems, apparatus, methods and articles are also described.

IPC 8 full level
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CPC (source: EP KR)
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Citation (search report)

- [IY] WO 2008139685 A1 20081120 - PANASONIC CORP [JP], et al
- [IPY] US 2010131791 A1 20100527 - KIMURA TOMOO [JP]
- [IY] US 2005198636 A1 20050908 - BARNES ERIC L [US], et al
- [Y] US 2007234091 A1 20071004 - VISHIN SANJAY [US], et al
- [Y] US 2009055829 A1 20090226 - GIBSON GARY A [US]
- [XYI] PADMANABHAN PILLAI ET AL: "Real-time dynamic voltage scaling for low-power embedded operating systems", PROCEEDINGS OF THE EIGHTEENTH ACM SYMPOSIUM ON OPERATING SYSTEMS PRINCIPLES, SOSP '01, 1 January 2001 (2001-01-01), New York, New York, USA, pages 89, XP055058610, ISBN: 978-1-58-113389-9, DOI: 10.1145/502034.502044
- [XYI] KIHWAN CHOI ET AL: "Dynamic Voltage and Frequency Scaling based on Workload Decomposition", PROCEEDINGS OF THE 2004 INTERNATIONAL SYMPOSIUM ON LOW POWER ELECTRONICS AND DESIGN. ISLPED'04. NEWPORT BEACH, CA, AUG. 9 - 11, 2004; [INTERNATIONAL SYMPOSIUM ON LOW POWER ELECTRONICS AND DESIGN], NEW YORK, NY : ACM, US, 9 August 2004 (2004-08-09), pages 174 - 179, XP010764300, ISBN: 978-1-58113-929-7
- See references of WO 2011120019A2

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