

Title (en)

Secure device anti-tampering circuit

Title (de)

Sicherungsschaltung für Sicherheitsvorrichtung

Title (fr)

Circuit de sécurisation d'un dispositif sûr

Publication

EP 2573716 A3 20151028 (EN)

Application

EP 12184989 A 20120919

Priority

US 201113237122 A 20110920

Abstract (en)

[origin: US8378710B1] Various embodiments relate to an anti-tampering circuit for a secure device including: a signal delay detector; a clock delay detector; a clock duty cycle detector; and a protection unit that receives an error indication from the signal delay detector, clock delay detector, and the clock duty cycle detector, wherein the protection unit indicates tampering to a secure device upon receiving the error indication.

IPC 8 full level

G06K 19/073 (2006.01)

CPC (source: EP US)

G06K 19/07372 (2013.01 - EP US)

Citation (search report)

- [A] US 2008309396 A1 20081218 - LEE SEUNG-WON [KR]
- [A] US 2009108878 A1 20090430 - YAMADA KENTA [JP]
- [A] DE 10326089 B3 20041118 - INFINEON TECHNOLOGIES AG [DE]

Cited by

GB2595112A; GB2595112B; US11022637B2; US11621234B2; WO2020144478A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

US 8378710 B1 20130219; CN 103198347 A 20130710; CN 103198347 B 20150923; EP 2573716 A2 20130327; EP 2573716 A3 20151028; EP 2573716 B1 20190605

DOCDB simple family (application)

US 201113237122 A 20110920; CN 201210350049 A 20120919; EP 12184989 A 20120919