

Title (en)

INTERPOSER AND METHOD FOR PRODUCING HOLES IN AN INTERPOSER

Title (de)

INTERPOSER UND VERFAHREN ZUR HERSTELLUNG VON LÖCHERN IN EINEM INTERPOSER

Title (fr)

INTERPOSEUR ET PROCÉDÉ POUR RÉALISER DES TROUS DANS UN INTERPOSEUR

Publication

EP 2589072 A2 20130508 (DE)

Application

EP 11730223 A 20110704

Priority

- DE 102010025966 A 20100702
- EP 2011003300 W 20110704

Abstract (en)

[origin: WO2012000685A2] Interposer for electrical connection between a CPU chip and a circuit board. A board-shaped base substrate (1) composed of glass has a coefficient of thermal expansion in the range of between $3.1 \cdot 10^{-6}$ and $3.4 \cdot 10^{-6}$ and holes (12) in a number that is in the range of between 10 and 10,000 cm⁻². There are holes (12) having diameters which can be in the range of between 20 μm and 200 μm . Conductor tracks (13) run on one board side, said conductor tracks in each case extending right into the holes (12) and through the latter to the other board side in order to form connection points for the chip.

IPC 8 full level

H01L 21/48 (2006.01); **H01L 23/15** (2006.01); **H01L 23/498** (2006.01)

CPC (source: EP KR US)

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Citation (search report)

See references of WO 2012000685A2

Designated contracting state (EPC)

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