

Title (en)
SEMICONDUCTOR DEVICE AND STRUCTURE

Title (de)
HALBLEITERVORRICHTUNG UND STRUKTUR DAMIT

Title (fr)
DISPOSITIF À SEMI-CONDUCTEURS, ET STRUCTURE ASSOCIÉE

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Application
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- US 97060210 A 20101216
- US 95192410 A 20101122
- US 95191310 A 20101122
- US 94961710 A 20101118
- US 94107510 A 20101107
- US 94107410 A 20101107
- US 94107310 A 20101107
- US 90410810 A 20101013
- US 90411910 A 20101013
- US 90189010 A 20101011
- US 90037910 A 20101007
- US 89425210 A 20100930
- US 85966510 A 20100819
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Abstract (en)

[origin: WO2012015550A2] A method for fabrication of semiconductor device comprising a first wafer comprising first single crystal layer comprising first transistors, first alignment marks, and first transistors interconnect layers comprising at least one metal layer overlying said first single crystal silicon layer, wherein said at least one metal layer comprises copper or aluminum; and comprising a step of implant and high temperature activation to form a conductive layer within a second wafer; and forming a second crystallized layer on top of said first wafer by transferring said conductive layer using ion-cut process, and forming second transistors on said second crystallized layer wherein said second transistors source and drain comprises portion of said first conductive layer.

IPC 8 full level

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C-Set (source: EP)

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2. **H01L 2224/131** + **H01L 2924/014**
3. **H01L 2924/10253** + **H01L 2924/00**
4. **H01L 2224/73204** + **H01L 2224/16145** + **H01L 2224/32145** + **H01L 2924/00**
5. **H01L 2924/1461** + **H01L 2924/00**
6. **H01L 2924/12032** + **H01L 2924/00**
7. **H01L 2924/1305** + **H01L 2924/00**
8. **H01L 2924/13062** + **H01L 2924/00**
9. **H01L 2224/73204** + **H01L 2224/16225** + **H01L 2224/32225** + **H01L 2924/00**
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