

Title (en)

CIRCUIT ARRANGEMENT AND METHOD FOR LIMITING THE CURRENT LEVEL AND/OR FLANK GRADIENT OF ELECTRICAL SIGNALS

Title (de)

SCHALTUNGSANORDNUNG UND VERFAHREN ZUR BEGRENZUNG VON STROMSTÄRKE UND/ODER FLANKENSTEILHEIT
ELEKTRISCHER SIGNALE

Title (fr)

CIRCUIT ÉLECTRIQUE ET PROCÉDÉ POUR LIMITER L'INTENSITÉ DU COURANT ET/OU LA PENTE DE SIGNAUX ÉLECTRIQUES

Publication

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Application

EP 11724182 A 20110603

Priority

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Abstract (en)

[origin: WO2012013402A1] A circuit arrangement (100) for limiting the current level and/or flank gradient of electrical signals, having a voltage source (116) and a switching element (102), connected to the voltage source (116) and designed for switching the voltage source (116), wherein the switching arrangement (100) furthermore has a limiting unit (120), wherein the limiting unit (120) is functionally arranged between the switching element (102) and the voltage source (116) and wherein the limiting unit (120) is designed to limit a current level and/or flank gradient of an electrical signal, using the switching element (102), during a switching process of the voltage source (116).

IPC 8 full level

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CPC (source: EP US)

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