

Title (en)
MULTILAYER MEMORY ARRAY

Title (de)
MEHRSCHICHTIGER SPEICHERARRAY

Title (fr)
MATRICE MÉMOIRE MULTICOUCHE

Publication
EP 2612357 A4 20150304 (EN)

Application
EP 10856793 A 20100830

Priority
US 2010047115 W 20100830

Abstract (en)
[origin: WO2012030320A1] A multilayer crossbar memory array includes a number of layers (514). Each layer (514) includes a top set of parallel lines, a bottom set of parallel lines intersecting the top set of parallel lines, and memory elements (200, 216) disposed at intersections between the top set of parallel lines and the bottom set of parallel lines. A top set of parallel lines from one of the layers (514) is a bottom set of parallel lines for an adjacent one of the layers (514).

IPC 8 full level
G11C 13/00 (2006.01); **H01L 27/10** (2006.01); **H01L 27/102** (2006.01); **H01L 27/24** (2006.01); **H01L 45/00** (2006.01)

CPC (source: EP KR US)
G11C 13/0007 (2013.01 - EP KR US); **G11C 13/0069** (2013.01 - EP US); **H01L 27/101** (2013.01 - EP KR US); **H01L 27/1021** (2013.01 - EP KR US); **H10B 63/20** (2023.02 - EP US); **H10B 63/84** (2023.02 - EP KR US); **H10N 70/20** (2023.02 - KR); **H10N 70/245** (2023.02 - KR); **H10N 70/826** (2023.02 - KR); **H10N 70/8833** (2023.02 - KR); **H10N 70/8836** (2023.02 - KR); **G11C 5/063** (2013.01 - US); **G11C 13/0002** (2013.01 - US); **G11C 13/004** (2013.01 - US); **G11C 13/02** (2013.01 - US); **G11C 2013/0073** (2013.01 - EP US); **G11C 2213/71** (2013.01 - EP US); **G11C 2213/77** (2013.01 - EP US); **H10B 63/00** (2023.02 - US); **H10B 63/80** (2023.02 - US); **H10N 70/24** (2023.02 - EP US); **H10N 70/826** (2023.02 - EP US); **H10N 70/841** (2023.02 - EP US); **H10N 70/883** (2023.02 - US); **H10N 70/8833** (2023.02 - EP US); **H10N 70/8836** (2023.02 - EP US)

Citation (search report)

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- [X] US 2007285971 A1 20071213 - TODA HARUKI [JP], et al
- [A] WO 2010074685 A1 20100701 - HEWLETT PACKARD DEVELOPMENT CO [US], et al
- [A] WO 2010014064 A1 20100204 - HEWLETT PACKARD DEVELOPMENT CO [US], et al
- [A] US 2009218565 A1 20090903 - KAWANO HIROYASU [JP], et al
- See references of WO 2012030320A1

Designated contracting state (EPC)
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