

Title (en)

METHOD FOR CORRECTING AN ASYMMETRY IN A DELAY

Title (de)

VERFAHREN ZUR KORREKTUR EINER VERZÖGERUNGSASYMMETRIE

Title (fr)

PROCEDE DE CORRECTION D'UNE ASYMETRIE DE DELAI

Publication

EP 2619936 A1 20130731 (FR)

Application

EP 11773098 A 20110915

Priority

- FR 1003727 A 20100920
- FR 2011052126 W 20110915

Abstract (en)

[origin: WO2012038644A1] The embodiments of the present invention relate to a method for correcting an asymmetry in a delay of synchronization messages transmitted within a network for switching packets between a master clock and a slave clock, in which the asymmetry in the delay of the path connecting the master clock to the slave clock is determined and locally corrected at at least one link of said path by means of a means for measuring and correcting a time shift, said means being located on the nodes of the path.

IPC 8 full level

H04J 3/06 (2006.01)

CPC (source: EP KR US)

H04J 3/06 (2013.01 - KR); **H04J 3/0635** (2013.01 - US); **H04J 3/0667** (2013.01 - EP US); **H04J 3/0673** (2013.01 - EP US);
H04J 14/0226 (2013.01 - EP US); **H04L 43/0852** (2013.01 - EP US)

Citation (search report)

See references of WO 2012038644A1

Citation (examination)

EP 1802014 A1 20070627 - AGILENT TECHNOLOGIES INC [US]

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)

FR 2965131 A1 20120323; FR 2965131 B1 20120928; CN 103119872 A 20130522; EP 2619936 A1 20130731; JP 2013538022 A 20131007;
JP 5671619 B2 20150218; KR 101479483 B1 20150106; KR 20130064808 A 20130618; US 2013209096 A1 20130815;
WO 2012038644 A1 20120329

DOCDB simple family (application)

FR 1003727 A 20100920; CN 201180045106 A 20110915; EP 11773098 A 20110915; FR 2011052126 W 20110915;
JP 2013528748 A 20110915; KR 20137010084 A 20110915; US 201113819363 A 20110915