

Title (en)

METHOD AND APPARATUS FOR USING ENTROPY IN ANT COLONY OPTIMIZATION CIRCUIT DESIGN FROM HIGH LEVEL SYNTESIS

Title (de)

VERFAHREN UND VORRICHTUNG ZUR VERWENDUNG EINER ENTROPIE ZUM ENTWURF EINER OPTIMIERUNGSSCHALTUNG FÜR EINE AMEISENKOLONIE AUS EINER SYNTHESE HOHER ORDNUNG

Title (fr)

PROCÉDÉ ET APPAREIL PERMETTANT D'UTILISER L'ENTROPIE DANS UN CONCEPT DE CIRCUIT D'OPTIMISATION DU TYPE COLONIE DE FOURMIS À PARTIR D'UNE SYNTÈSE DE HAUT NIVEAU

Publication

**EP 2622549 A4 20140423 (EN)**

Application

**EP 11832913 A 20110831**

Priority

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- US 89484210 A 20100930
- US 89475610 A 20100930
- US 2011050081 W 20110831

Abstract (en)

[origin: WO2012050678A1] A method and apparatus for using entropy in ant colony optimization circuit design from high level synthesis is described. In one example, an operation to be performed by a circuit is selected. A plurality of hardware components for performing the operation are represented with a data flow graph having edges and nodes. A plurality of solutions for performing the operation are simulated as hardware component combinations represented as paths on the data flow graph. For each solution the cost including a number of edges and nodes traversed on the data flow graph and a supplemental sub-integer cost is determined and a solution is selected with the lowest cost as a hardware component combination for a circuit.

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

- [I] US 7284228 B1 20071016 - HARATSARIS PHILIP R [US]
- [X] GANG WANG ET AL: "Ant Colony Optimizations for Resource-and Timing-Constrained Operation Scheduling", IEEE TRANSACTIONS ON COMPUTER AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS, IEEE SERVICE CENTER, PISCATAWAY, NJ, US, vol. 26, no. 6, 1 June 2007 (2007-06-01), pages 1010 - 1029, XP011180071, ISSN: 0278-0070, DOI: 10.1109/TCAD.2006.885829
- See references of WO 2012050678A1

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