

Title (en)

METHOD AND CIRCUIT ARRANGEMENT FOR TRANSMITTING DATA BETWEEN PROCESSOR MODULES

Title (de)

VERFAHREN UND SCHALTUNGSAORDNUNG ZUR DATENÜBERTRAGUNG ZWISCHEN PROZESSORBAUSTEINEN

Title (fr)

PROCÉDÉ ET CIRCUIT POUR LA TRANSMISSION DE DONNÉES ENTRE MODULES DE PROCESSEURS

Publication

EP 2641183 A1 20130925 (DE)

Application

EP 11741215 A 20110805

Priority

- DE 102010043929 A 20101115
- DE 102011007437 A 20110414
- EP 2011063574 W 20110805

Abstract (en)

[origin: WO2012065760A1] The invention relates to a circuit arrangement (5) for forming a digital interface (120, 121, 122, 123) comprising a digital data bus (123), which exchanges data when microprocessor systems are connected, wherein said data exchange can be effected bidirectionally. On transmission of data the circuit arrangement generates as bus master a bus clock speed and operates on receipt of data as a bus slave in accordance with a received clock signal. The circuit arrangement comprises at least one FIFO memory (101) for transmitting data and/or at least one FIFO memory (104) for receiving data.

IPC 8 full level

G06F 13/42 (2006.01)

CPC (source: EP KR US)

G06F 13/364 (2013.01 - EP US); **G06F 13/38** (2013.01 - KR); **G06F 13/42** (2013.01 - KR); **G06F 13/4265** (2013.01 - EP US)

Citation (search report)

See references of WO 2012065760A1

Citation (examination)

- US 5592629 A 19970107 - GAMBLE WILLIAM H [US]
- ANONYMOUS: "The I2C specification 2.1", 1.1.2000., 1 January 2000 (2000-01-01), XP030001520, ISSN: 0000-0097

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)

DE 102011007437 A1 20120516; CN 103210384 A 20130717; CN 103210384 B 20160824; EP 2641183 A1 20130925;
KR 20130129388 A 20131128; US 2013262724 A1 20131003; WO 2012065760 A1 20120524

DOCDB simple family (application)

DE 102011007437 A 20110414; CN 201180054892 A 20110805; EP 11741215 A 20110805; EP 2011063574 W 20110805;
KR 20137015524 A 20110805; US 201113885315 A 20110805