

Title (en)

Method for manufacturing a SMD resistor

Title (de)

Verfahren zur Herstellung eines SMD Widerstands

Title (fr)

Procédé de fabrication d'une résistance SMD

Publication

EP 2680279 B1 20200101 (EN)

Application

EP 13159913 A 20130319

Priority

TW 101122620 A 20120625

Abstract (en)

[origin: EP2680279A1] In a method of manufacturing a chip resistor (2), a semi-product (43) is formed by sandwiching an electric-insulating material layer (5) between an electric-conducting material layer (41) and a heat-dissipating material layer (42). Resistor sections (46) arranged in an array on the semi-product (43) are formed by forming longitudinal first slots (44) and transverse second slots (45) through the semi-product (43). Slits (211) are formed on a first layer (411) of each resistor section to form a resistor main body (21). A dividing slot (231) is formed on a second layer (421) of each resistor section (46). Two electrodes (24) are formed to be electrically connected to opposite ends (214) of the resistor main body (21). The resistor sections (46) are trimmed from the semi-product (43) to obtain the chip resistors (2). Fig. 3

IPC 8 full level

H01C 7/00 (2006.01); **H01C 17/00** (2006.01); **H01C 17/22** (2006.01)

CPC (source: EP KR US)

H01C 7/00 (2013.01 - KR US); **H01C 17/006** (2013.01 - US); **H01C 17/06** (2013.01 - KR); **H01C 17/22** (2013.01 - EP US);
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US 2009322467 A1 20091231 - HETZLER ULLRICH [DE]

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