

Title (en)  
SYSTEMS AND METHODS FOR ADJUSTING TIME SLOTS OF VECTORING STREAMS BASED ON BIT LOADING

Title (de)  
SYSTEME UND VERFAHREN ZUR EINSTELLUNG DER ZEITSCHLITZE VON VEKTORIERUNGSSTRÖMEN AUF BASIS VON BITLADUNGEN

Title (fr)  
SYSTÈMES ET PROCÉDÉS DESTINÉS À AJUSTER LES INTERVALLES DE TEMPS DES TRAINS DE VECTORISATION EN FONCTION DU CHARGEMENT DES BITS

Publication  
**EP 2681861 A4 20141112 (EN)**

Application  
**EP 12753037 A 20120302**

Priority  
• US 201161448546 P 20110302  
• US 2012027576 W 20120302

Abstract (en)  
[origin: US2012224674A1] A communication system comprises a plurality of line cards having transceivers coupled to a plurality of subscriber lines. Each line card has at least one active transceiver within the same vectoring group, and each line card also has vector logic capable of cancelling crosstalk induced by an active transceiver that is a member of the vectoring group. In the event of a vectoring fault that prevents a line card from receiving vectoring information from at least on other line card, the vector logic is configured to disable vectoring for the interferers affected by the error in order to prevent vectoring operations based on obsolete vectoring coefficients from adversely affecting the quality of the communicated signals. The transceivers communicating signals affected by the suspended vectoring operations are also configured to adjust their constellation density profiles, thereby reducing their data rates, to accommodate the increased noise level resulting from the loss of vectoring. By handling the vectoring fault in such manner, communication can continue without requiring a retrain.

IPC 8 full level  
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CPC (source: EP US)  
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Citation (search report)  
• [X] US 2006280237 A1 20061214 - RHEE WONJONG [US], et al  
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• See references of WO 2012119115A2

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