

Title (en)

DUAL ACTIVE LAYERS FOR SEMICONDUCTOR DEVICES AND METHODS OF MANUFACTURING THE SAME

Title (de)

ZWEIFACH AKTIVE SCHICHTEN FÜR HALBLEITERVORRICHTUNGEN UND HERSTELLUNGSVERFAHREN DAFÜR

Title (fr)

COUCHES ACTIVES DOUBLES POUR DISPOSITIFS À SEMI-CONDUCTEUR ET SES PROCÉDÉS DE FABRICATION

Publication

**EP 2695195 A2 20140212 (EN)**

Application

**EP 12767894 A 20120405**

Priority

- US 201161472992 P 20110407
- US 2012032388 W 20120405

Abstract (en)

[origin: WO2012138903A2] Some embodiments include dual active layers for semiconductor devices. Other embodiments of related devices and methods are also disclosed.

IPC 8 full level

**H01L 29/786** (2006.01)

CPC (source: CN EP KR)

**G02F 1/1368** (2013.01 - KR); **G02F 1/167** (2013.01 - KR); **H01L 29/7869** (2013.01 - CN EP KR); **H01L 29/78696** (2013.01 - EP KR)

Cited by

RU2749331C1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)

**WO 2012138903 A2 20121011**; **WO 2012138903 A3 20130131**; CN 103548146 A 20140129; EP 2695195 A2 20140212; EP 2695195 A4 20140917; JP 2014513425 A 20140529; KR 20130138328 A 20131218; SG 194073 A1 20131129

DOCDB simple family (application)

**US 2012032388 W 20120405**; CN 201280017495 A 20120405; EP 12767894 A 20120405; JP 2014504000 A 20120405; KR 20137029528 A 20120405; SG 2013074125 A 20120405