Title (en)
SYSTEMS AND METHODS INVOLVING MULTI-BANK, DUAL- OR MULTI-PIPE SRAMS

Title (de)
SYSTEME UND VERFAHREN MIT SRAM MIT MEHREREN BANKEN UND ZWEI ODER MEHREREN LEITUNGEN

Title (fr)
SYSTÈMES ET PROCÉDÉS METTANT EN UVRE DES MÉMOIRES SRAM EN BATTERIES MULTIPLES, À GUIDES DOUBLES OU MULTIPLES

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Application
EP 12823364 A 20120813

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Abstract (en)
[origin: US2013039131A1] Systems and methods are disclosed for increasing the performance of static random access memory (SRAM). Various systems herein, for example, may include or involve dual- or multi-pipe, multi-bank SRAMs, such as Quad-B2 SRAMs. In one illustrative implementation, there is provided an SRAM memory device including a memory array comprising a plurality of SRAM banks and pairs of separate and distinct pipes associated with each of the SRAM banks, wherein each pair of pipes may provide independent access to its associated SRAM bank.

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Citation (search report)
• [A] US 6167487 A 20001226 - CAMACHO STEPHEN [US], et al
• [A] US 2004264286 A1 20041230 - WARE FREDERICK A [US], et al
• See references of WO 2013025656A1

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US 2013039131 A1 20130214; **US 8982649 B2 20150317**; CN 104025196 A 20140903; CN 104025196 B 20170503; EP 2742506 A1 20140618; EP 2742506 A4 20150311; EP 2742506 B1 20181017; US 2014304463 A1 20141009; US 2016189766 A1 20160630; US 9196324 B2 20151124; US 9679631 B2 20170613; WO 2013025656 A1 20130221

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US 201113327721 A 20111215; CN 201280050486 A 20120813; EP 12823364 A 20120813; US 2012050667 W 20120813; US 201414170497 A 20140131; US 201514951160 A 20151124