Title (en)

MODULAR CONVERTER ARRANGEMENT

Title (de

MODULARE UMRICHTERANORDNUNG

Title (fr)

**CONVERTISSEUR MODULAIRE** 

Publication

EP 2756588 A1 20140723 (DE)

Application

EP 11785644 A 20111114

Priority

EP 2011070055 W 20111114

Abstract (en)

[origin: WO2013071947A1] The invention relates inter alia to a converter arrangement (10) comprising a first terminal side (11), on which at least one alternating current can be fed in and/or at least one ac voltage can be applied, and a second terminal side (12), on which a converted alternating or direct current formed by conversion can be tapped, wherein the converter arrangement (10) comprises at least two series circuits (R1, R2, R3) arranged in parallel, the outer terminals of which are connected to the second terminal side (12) of the converter arrangement (10) or form said second terminal side, wherein each of the series circuits (R1, R2, R3) arranged in parallel comprises at least two switching modules (SM) arranged in series, and wherein a control device (30), which can control the switching modules (SM) in such a way that branch currents flow into the series circuits (R1, R2, R3) and the converted alternating or direct current on the second terminal side (12) has a predetermined magnitude and/ or a predetermined progression over time, is connected to the switching modules (SM). According to the invention, the converter arrangement (10) has a harmonics detection module (40) which detects at least one additional current harmonic by means of the respective converter operating state for each of the series circuits (R1, R2, R3), wherein the additional current harmonics are dimensioned such that they flow into the parallel-arranged series circuits (R1, R2, R3) of the converter arrangement (10) in the circuitry and remain within the converter arrangement (10), and the control unit (30) controls the switching modules (SM) in such a way that the at least one additional current harmonic detected by the harmonics detection module (40) flows in each of the series circuits (R1, R2, R3).

IPC 8 full level

H02M 7/49 (2007.01)

CPC (source: EP US)

H02M 7/483 (2013.01 - EP US); H02M 7/4835 (2021.05 - EP US)

Citation (search report)

See references of WO 2013071947A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)

WO 2013071947 A1 20130523; EP 2756588 A1 20140723

DOCDB simple family (application)

EP 2011070055 W 20111114; EP 11785644 A 20111114