Title (en)

Control circuit for a gas valve

Title (de)

Steuerschaltung für ein Gasventil

Title (fr)

Circuit de commande pour soupape à gaz

Publication

EP 2775207 A1 20140910 (EN)

Application

EP 13158436 A 20130308

Priority

EP 13158436 A 20130308

Abstract (en)

Control circuit (10) for a gas valve, the control circuit (10) comprising in-put contacts (14, 21) by which the control circuit is connectable to a microprocessor, output contacts (31, 32) by which the control circuit is connectable to the gas valve to be operated, a drive circuit (11) and a fail-safe circuit (12), wherein the drive circuit (11) comprises a first transistor (19) and a second transistor (20) both being operated through the fail-safe circuit (12) on basis of a first signal provided by the microprocessor. The first signal is provided by the microprocessor at a first input contact (21) of the control circuit, namely at an input contact (21) of the fail-safe circuit (12). A first parallel connection of a resistor (33), a capacitor (34) and a first and a second diode (35, 36) being serially connected is connected between the gate and the source of the first transistor (19). A second parallel connection of a resistor (33), a capacitor (34) and a first and a second diode (35, 36) being serially connected is connected between the gate and the source of the second transistor (20). A first series connection (37) having a resistor (38) and a capacitor (39) is connected with a first contact point between the first diode (35) and the second diode (36) assigned to the first transistor (19) and with a second contact point to a first output contact (27) of the fail-safe circuit (12). A second series connection (40) having a resistor (38) and a capacitor (39) is connected with a first contact point between the first diode (35) and the second diode (36) assigned to the second transistor (20) and with a second contact point to the first output contact (27) of the fail-safe circuit (12). A series connection of the first transistor (19) and the second tran-sistor (20) is connected between a power supply contact (18) and a sec-ond output contact (28) of the fail-safe circuit (12).

IPC 8 full level

F23N 5/24 (2006.01)

CPC (source: EP)

F23N 5/242 (2013.01); F23N 2227/16 (2020.01); F23N 2231/00 (2020.01)

Citation (applicant)

- EP 1730760 B1 20100728 - HONEYWELL TECHNOLOGIES SARL [CH]
- EP 1730432 A1 20061213 - HONEYWELL TECHNOLOGIES SARL [CH]

Citation (search report)

- [A] US 4865538 A 19890912 - SCHEELE VICTOR F [US], et al
- [AD] EP 1730760 B1 20100728 - HONEYWELL TECHNOLOGIES SARL [CH]
- [AD] EP 1730432 A1 20061213 - HONEYWELL TECHNOLOGIES SARL [CH]

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

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DOCDB simple family (application)

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