

Title (en)

METHOD AND SYSTEM FOR PRODUCING CIRCUIT BOARDS

Title (de)

VERFAHREN UND SYSTEM ZUR HERSTELLUNG VON LEITERPLATTEN

Title (fr)

PROCÉDÉ ET SYSTÈME POUR FABRIQUER DES CARTES DE CIRCUITS IMPRIMÉS

Publication

EP 2777369 A1 20140917 (DE)

Application

EP 12737711 A 20120629

Priority

- DE 102011118188 A 20111111
- EP 2012002750 W 20120629

Abstract (en)

[origin: WO2013068054A1] The invention relates to a method and to a system (1) for producing circuit boards (2) having pressed-in moulded parts, wherein at least one moulded part (3), before being pressed into a cutout (4) in a circuit board (2) by means of a pressing tool (6), is oriented by a template (7) relative to the cutout (4) in the circuit board (2).

IPC 8 full level

B23P 11/00 (2006.01); **B23P 19/02** (2006.01); **B23P 19/10** (2006.01); **H05K 1/02** (2006.01); **H05K 3/00** (2006.01)

CPC (source: EP)

H05K 1/0204 (2013.01); **H05K 3/0008** (2013.01); **H05K 2201/10416** (2013.01); **H05K 2203/0169** (2013.01); **H05K 2203/0195** (2013.01);
H05K 2203/0278 (2013.01)

Citation (search report)

See references of WO 2013068054A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)

WO 2013068054 A1 20130516; EP 2777369 A1 20140917; TW 201320848 A 20130516

DOCDB simple family (application)

EP 2012002750 W 20120629; EP 12737711 A 20120629; TW 101123089 A 20120627