Title (en)
   QUEUING APPARATUS

Title (de)
   WARTESCHLANGENVORRICHTUNG

Title (fr)
   APPAREIL DE MISE EN FILES D'ATTENTE

Publication
   EP 2783277 A1 20141001 (EN)

Application
   EP 11794131 A 20111207

Priority
   EP 2011072086 W 20111207

Abstract (en)
   [origin: WO2013083191A1] A queuing apparatus (1) having a queuing engine (2) comprising: a predetermined number, K, of queues, Q, wherein each queue Q has a number, N, of sub-queues, SQ, associated to a corresponding number, N, of input lanes of said queuing engine (2), wherein each sub-queue, SQ, is used for storing Packet Descriptors applied by each associated input lane of said queuing engine (2); and a shared descriptor memory (10) adapted to store all Descriptors of the sub-queues, SQ, of the predetermined number, K, of queues, Q, of said queuing engine (2), wherein in each system clock cycle of a system clock signal (CLK) applied to said queuing engine (2), up to N input lanes can request a write operation, WR, to queues, Q, and up to one read operation, RR, from any queue, Q.

IPC 8 full level
   G06F 5/06 (2006.01); **H04L 12/70** (2013.01)

CPC (source: EP)
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