

Title (en)

Capacitive level shifter devices, methods and systems

Title (de)

Kapazitive Pegelschiebervorrichtungen, Verfahren und Systeme

Title (fr)

Dispositifs, procédés et systèmes de décalage de niveau capacitif

Publication

EP 2787642 A2 20141008 (EN)

Application

EP 14158190 A 20140306

Priority

US 201313856184 A 20130403

Abstract (en)

Systems and methods of use relate to a circuit that is designed to detect the state of two control signals, wherein one control signal indicates an ON state for the gate driver and the other control signal indicates an OFF state for the gate driver. The circuit responds to each of the control signals by controlling the gate driver so that it drives an output either high or low. The circuit can also be configured to control the gate driver so that it drives the output (either high or low) when neither control signal is present.

IPC 8 full level

H03K 19/0175 (2006.01); **H03K 19/0185** (2006.01); **H03K 17/689** (2006.01)

CPC (source: EP US)

H02M 5/293 (2013.01 - US); **H03K 19/017509** (2013.01 - EP US); **H03K 19/018521** (2013.01 - EP US); **H03K 17/689** (2013.01 - EP US)

Cited by

KR20170139460A; KR20170130420A; CN114389449A; WO2016148782A1; US9912327B2; US10734982B2; US10971359B2; US9484897B2; US9843311B2; US9847348B1; US10672726B2; US10770480B2; US11251140B2; US11948897B2

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

EP 2787642 A2 20141008; **EP 2787642 A3 20170607**; **EP 2787642 B1 20200325**; CN 104104381 A 20141015; US 2014300188 A1 20141009; US 9467060 B2 20161011

DOCDB simple family (application)

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