

Title (en)
OPTOELECTRONIC SEMICONDUCTOR CHIP

Title (de)
OPTOELEKTRONISCHER HALBLEITERCHIP

Title (fr)
PUCE SEMI-CONDUCTRICE OPTOÉLECTRONIQUE

Publication
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Application
EP 13708711 A 20130220

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Abstract (en)
[origin: WO2013127672A1] The invention relates to an optoelectronic semiconductor chip, comprising a plurality of active regions (1) that are arranged at a distance from each other and a substrate (2) that is arranged on an underside (1a) of the plurality of active regions (1), wherein one of the active regions (1) has a main extension direction (R), the active region (1) has a core region (10) that is formed using a first semiconductor material, the active region (1) has an active layer (11) that which covers the core region (10) at least in directions (x, y) perpendicular to the main extension direction (R) of the active region (1), and the active region (1) has a cover layer (12) that is formed using a second semiconductor material and covers the active layer (11) at least in directions (x, y) perpendicular to the main extension direction (R) of the active region (11).

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Citation (search report)
See references of WO 2013127672A1

Citation (examination)
• WO 2011067872 A1 20110609 - UNIV HOKKAIDO NAT UNIV CORP [JP], et al & EP 2509119 A1 20121010 - UNIV HOKKAIDO NAT UNIV CORP [JP], et al
• WO 2011080219 A1 20110707 - OSRAM OPTO SEMICONDUCTORS GMBH [DE], et al

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