

Title (en)

SINGLE-CHIP SIGNAL SPLITTING CARRIER AGGREGATION RECEIVER ARCHITECTURE

Title (de)

SINGLE-CHIP-EMPFÄNGERARCHITEKTUR FÜR EINE SIGNALVERZWEIGUNGSTRÄGERAGGREGATION

Title (fr)

ARCHITECTURE DE RÉCEPTEUR À AGRÉGATION DE PORTEUSES À SÉPARATION DE SIGNAL MONOPUCE

Publication

EP 2820761 A1 20150107 (EN)

Application

EP 13714048 A 20130301

Priority

- US 201213411444 A 20120302
- US 2013028742 W 20130301

Abstract (en)

[origin: US2013231064A1] A wireless communication device configured for receiving a multiple carrier signal is described. The wireless communication device includes a single-chip signal splitting carrier aggregation receiver architecture. The single-chip signal splitting carrier aggregation receiver architecture includes a primary antenna, a secondary antenna and a transceiver chip. The single-chip signal splitting carrier aggregation receiver architecture reuses a simultaneous hybrid dual receiver path.

IPC 8 full level

H04B 1/00 (2006.01); **H04B 7/04** (2006.01)

CPC (source: EP US)

H04B 1/0057 (2013.01 - EP US); **H04B 7/04** (2013.01 - EP US)

Citation (search report)

See references of WO 2013131051A1

Cited by

CN109756319A

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

US 2013231064 A1 20130905; US 9362958 B2 20160607; CN 104137423 A 20141105; CN 104137423 B 20170503; EP 2820761 A1 20150107; EP 2820761 B1 20160504; JP 2015516711 A 20150611; JP 6246743 B2 20171213; KR 102032524 B1 20191015; KR 20140131573 A 20141113; WO 2013131051 A1 20130906

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US 201213411444 A 20120302; CN 201380011850 A 20130301; EP 13714048 A 20130301; JP 2014560109 A 20130301; KR 20147027660 A 20130301; US 2013028742 W 20130301