

Title (en)
REVERSIBLE MATRIX CONVERTER CIRCUIT

Title (de)
WANDLERSCHALTUNG MIT UMKEHRBARER MATRIX

Title (fr)
CIRCUIT CONVERTISSEUR MATRICIEL REVERSIBLE

Publication
EP 2862266 A2 20150422 (FR)

Application
EP 13737162 A 20130618

Priority
• FR 1255667 A 20120618
• EP 2013062667 W 20130618

Abstract (en)
[origin: WO2013189952A2] The invention relates to a reversible matrix converter circuit with n levels per phase comprising n conversion arms exhibiting on one side n ends for generating or receiving respectively n intermediate DC voltage levels, and exhibiting on another side n ends linked at a common point of AC signal input or output. This circuit comprises:- two external arms linked respectively to the highest level of positive voltage and to the lowest level of negative voltage, these two external arms each comprising a single IGBT transistor or two power transistors, linked by their emitter, - two IGBT power transistors, linked in series by their emitter on each of the n-1 internal arms, - filtering capacitors disposed respectively between the n intermediate voltage levels.

IPC 8 full level
H02M 5/458 (2006.01); **H02M 7/483** (2007.01)

CPC (source: EP US)
H02M 5/293 (2013.01 - EP US); **H02M 5/4585** (2013.01 - EP US); **H02M 7/483** (2013.01 - EP US); **H02M 7/797** (2013.01 - US)

Citation (search report)
See references of WO 2013189952A2

Citation (examination)
US 2008285314 A1 20081120 - KOJORI HASSAN ALI [CA]

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)
BA ME

DOCDB simple family (publication)
FR 2992116 A1 20131220; FR 2992116 B1 20151113; EP 2862266 A2 20150422; US 2015131348 A1 20150514; US 9577549 B2 20170221; WO 2013189952 A2 20131227; WO 2013189952 A3 20140306

DOCDB simple family (application)
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