

Title (en)
DEVICE FOR FREQUENCY TRIPPLING

Title (de)
VORRICHTUNG ZUR FREQUENZVERDREIFACHUNG

Title (fr)
DISPOSITIF DESTINÉ À TRIPLER UNE FRÉQUENCE

Publication
EP 2885871 A1 20150624 (EN)

Application
EP 12748475 A 20120820

Priority
EP 2012066171 W 20120820

Abstract (en)
[origin: WO2014029413A1] A frequency tripler (100) comprises an input port (150) and an output port (155), and a first (140) and a second (145) transistor of the bipolar junction kind. The base of the first transistor (140) is connected to the input port (150) and the collector of the second transistor is connected to the output port (155). The emitters of the first (140) and second (145) transistors are grounded, and the collector of the first transistor (140) is connected to the base of the second transistor (145).

IPC 8 full level
H03B 19/14 (2006.01)

CPC (source: EP)
H03B 19/14 (2013.01)

Citation (search report)
See references of WO 2014029413A1

Cited by
EP3709508A4; US11139841B2

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)
BA ME

DOCDB simple family (publication)
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