

Title (en)

NETWORK ON A CHIP SOCKET PROTOCOL

Title (de)

PROTOKOLL FÜR EINE STECKDOSE MIT EINEM NETZWERK AUF EINEM CHIP

Title (fr)

PROTOCOLE DE SOCKET DE RÉSEAU SUR PUCE

Publication

EP 2901294 A4 20160810 (EN)

Application

EP 13842232 A 20130924

Priority

- US 201213626758 A 20120925
- US 201213626766 A 20120925
- US 2013061295 W 20130924

Abstract (en)

[origin: WO2014052261A1] The invention is a transaction interface protocol wherein the interface protocol has a transaction identifier signal in each of the request and response channels. It is used between a target network interface unit (NIU) master and an initiator NIU slave that are directly connected through a transaction interface. The target NIU response channel uses the transaction ID signal to identify the entry in a context array associated with the corresponding request. The coupling of target NIU and initiator NIU enable the formation of an on-chip interconnect comprising multiple network-on-chip (NoCs) wherein the topology of the interconnect is simpler, smaller, faster, and has lower latency.

IPC 8 full level

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CPC (source: CN EP)

G06F 15/1735 (2013.01 - EP); **G06F 15/7825** (2013.01 - EP); **H04L 12/403** (2013.01 - CN EP)

Citation (search report)

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- [I] XU YANG, ZHANG QING LI, FU FANG-FA , YU MING-YAN, LIU CHENG: "NISAR: An AXI Compliant On-chip NI Architecture Offering Transaction Reordering Processing", 25 October 2007 (2007-10-25), XP002759391, Retrieved from the Internet <URL:<http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4415774>> [retrieved on 20160630]
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- See references of WO 2014052261A1

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AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)

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DOCDB simple family (application)

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