

Title (en)

VERSATILE SIL2 DETECTOR HAVING TWO OUTPUTS AND ONE TEST INPUT

Title (de)

VIELSEITIGER SIL2-DETEKTOR MIT ZWEI AUSGÄNGEN UND EINEM TESTEINGANG

Title (fr)

DÉTECTEUR SIL2 POLYVALENT DOTÉ DE DEUX SORTIES ET D'UNE ENTRÉE DE TEST

Publication

EP 2912646 A1 20150902 (FR)

Application

EP 13770668 A 20130924

Priority

- FR 1260248 A 20121026
- EP 2013069853 W 20130924

Abstract (en)

[origin: WO2014063889A1] The invention relates to a detector (D1) including: a detection stage (1) connected to a sensor member (10), to be used for generating a detection signal; a first power supply terminal (A1) and a second power supply terminal (A2); a first output terminal (O1) connected to a first output; a second output terminal (O2) connected to a second output; a processing stage (12) for processing the detection signal and controlling the first output and the second output based on the received detection signal; a test module (MT) connected to the first power supply terminal (A1) or the second power supply terminal and configured to receive a test input signal (S_test) from the first power supply terminal or the second power supply terminal, wherein said test module is configured to generate an output signal (Sig) configured to place each output in a predetermined state that can be interpreted by an external logical processing unit (2).

IPC 8 full level

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CPC (source: CN EP)

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Citation (search report)

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