

Title (en)  
CIRCUIT FOR GENERATING A REFERENCE VOLTAGE

Title (de)  
ERZEUGUNGSSCHALTKEIS EINER BEZUGSSPANNUNG

Title (fr)  
Circuit de génération d'une tension de référence

Publication  
**EP 2930583 A3 20151216 (FR)**

Application  
**EP 15160418 A 20150323**

Priority  
FR 1453014 A 20140404

Abstract (en)  
[origin: US2015286238A1] A reference voltage generation circuit, including a first current source in series with a first bipolar transistor; a second current source in series with a first resistor; a third current source in series with a second bipolar transistor, the third current source being assembled as a current mirror with the first current source; a second resistor between the base of the second bipolar transistor and the junction point between the current source and the first resistor; and a fourth current source in series with a third resistor, the junction point between the fourth current source and the third resistor defining a reference voltage terminal.

IPC 8 full level  
**G05F 3/30** (2006.01)

CPC (source: EP US)  
**G05F 3/16** (2013.01 - US); **G05F 3/26** (2013.01 - EP US); **G05F 3/267** (2013.01 - EP US); **G05F 3/30** (2013.01 - EP US)

Citation (search report)

- [IA] US 4590419 A 19860520 - MORIARTY JR JOHN K [US]
- [A] US 2009146730 A1 20090611 - CHEN BO-WEI [TW]
- [A] US 2011169561 A1 20110714 - CHU KWAN-JEN [TW], et al
- [AD] US 2012153997 A1 20120621 - BLANC JEAN-PIERRE [FR], et al
- [AD] US 7408400 B1 20080805 - DESTASI FRANK [US]

Designated contracting state (EPC)  
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)  
BA ME

DOCDB simple family (publication)  
**EP 2930583 A2 20151014; EP 2930583 A3 20151216; EP 2930583 B1 20190130**; FR 3019660 A1 20151009; US 2015286238 A1 20151008; US 9588538 B2 20170307

DOCDB simple family (application)  
**EP 15160418 A 20150323**; FR 1453014 A 20140404; US 201514675309 A 20150331