

Title (en)  
DISPLAYING A FORGERY-PROOF IDENTITY INDICATOR

Title (de)  
ANZEIGE EINES FÄLSCHUNGSICHEREN IDENTITÄTSINDIKATORS

Title (fr)  
AFFICHAGE D'UN INDICATEUR D'IDENTITÉ INFALSIFIABLE

Publication  
**EP 2936478 A1 20151028 (DE)**

Application  
**EP 13821466 A 20131220**

Priority  

- EP 12199132 A 20121221
- EP 2013077635 W 20131220
- EP 13821466 A 20131220

Abstract (en)  
[origin: EP2747071A1] The method involves drawing a graphics (32) that is to be represented on a monitor (30), by client applications (11, 12) in a client specific region (22) i.e. overlay buffer, of a memory e.g. frame buffer, of a graphics circuit. A tamper-resistant identity indicator (31) that belongs to the client applications, is drawn in a memory area (21) outside the client specific region. The client specific region is displayed on the monitor by using a display controller (20). The indicator from the memory area is simultaneously displayed on the monitor using an overlay functionality of the controller. Independent claims are also included for the following: (1) a computer system (2) a computer program product comprising instructions for performing a method for displaying a tamper-resistant identity indicator on a monitor.

IPC 8 full level  
**G09G 5/36** (2006.01); **G06F 3/14** (2006.01); **G06F 21/52** (2013.01); **G06F 21/57** (2013.01); **G09G 5/14** (2006.01); **G09G 5/397** (2006.01)

CPC (source: EP US)  
**G06F 3/14** (2013.01 - EP US); **G06F 21/57** (2013.01 - EP US); **G06F 21/64** (2013.01 - EP US); **G06T 1/60** (2013.01 - US); **G09G 5/36** (2013.01 - EP US); **G09G 5/363** (2013.01 - US); **G09G 5/39** (2013.01 - US); **G09G 5/397** (2013.01 - EP US); **H04N 5/44504** (2013.01 - EP US); **H04N 21/42653** (2013.01 - EP US); **H04N 21/4753** (2013.01 - EP US); **G09G 5/14** (2013.01 - EP US); **G09G 2340/12** (2013.01 - EP US); **G09G 2358/00** (2013.01 - EP US); **G09G 2360/18** (2013.01 - US)

Citation (search report)  
See references of WO 2014096334A1

Citation (examination)  

- US 2005168471 A1 20050804 - PAQUETTE MICHAEL J [US]
- MACKENNA C ET AL: "A BIT-MAPPED PROCESSOR PROVIDING HARDWARE WINDOWING AND FAST TEXT DISPLAY", WESCON TECHNICAL PAPERS, WESTERN PERIODICALS CO. NORTH HOLLYWOOD, US, vol. 30, 18 November 1986 (1986-11-18), pages 3/0201 - 14, XP000111653

Designated contracting state (EPC)  
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)  
BA ME

DOCDB simple family (publication)  
**EP 2747071 A1 20140625**; CN 105340000 A 20160217; CN 105340000 B 20180605; EP 2936478 A1 20151028; KR 102071100 B1 20200129; KR 20150096804 A 20150825; US 10755677 B2 20200825; US 2015348515 A1 20151203; WO 2014096334 A1 20140626

DOCDB simple family (application)  
**EP 12199132 A 20121221**; CN 201380067384 A 20131220; EP 13821466 A 20131220; EP 2013077635 W 20131220; KR 20157019900 A 20131220; US 201314653858 A 20131220