

Title (en)

METHOD OF DECOYING OF A SYSTEM FOR INTERCEPTION AND JAMMING BY INSERTING DUMMY SYNCHRONIZATION PATTERNS INTO THE SIGNAL EMITTED AND EMITTER IMPLEMENTING THE METHOD

Title (de)

VERFAHREN ZUR ANLOCKUNG EINES SYSTEMS ZUM ABFANGEN UND STÖREN DURCH EINFÜGEN VON DUMMY-SYNCHRONISATIONSMUSTERN IN DAS EMITTIERTE SIGNAL UND EMITTER ZUR UMSETZUNG DES VERFAHRENS

Title (fr)

PROCÉDÉ DE LEURRAGE D' UN SYSTÈME D' INTERCEPTION ET DE BROUILLAGE PAR INSERTION DE MOTIFS DE SYNCHRONISATION FACTICES DANS LE SIGNAL ÉMIS ET ÉMETTEUR METTANT EN OEUVRE LE PROCÉDÉ

Publication

**EP 2936715 A1 20151028 (FR)**

Application

**EP 13805372 A 20131213**

Priority

- FR 1203478 A 20121219
- EP 2013076586 W 20131213

Abstract (en)

[origin: WO2014095651A1] Method of decoying of a system for interception and/or jamming, consisting in generating, in a signal to be emitted, a dummy sequence (SYNCF), said method comprising the following steps: - defining said dummy sequence (SYNCF) and its temporal and/or frequential position within the signal to be emitted, the values of the symbols of said dummy sequence and their temporal and/or frequential positions being different from those of the symbols of a synchronization sequence (SYNC) that the said signal comprises, - estimating the value and the position of the dummy bits (BF) to be inserted within the data sequence (Du, T) to be emitted at the input of the emission chain so as to obtain, in the sequence produced at the output of the emission chain, the predetermined value and predetermined temporal position of the symbols of said dummy sequence (SYNCF), - inserting, within said data sequence (Du, T), the dummy bits (BF) at the positions obtained.

IPC 8 full level

**H04K 3/00** (2006.01)

CPC (source: EP)

**H04K 3/28** (2013.01); **H04K 3/65** (2013.01); **H04K 3/825** (2013.01); **H04K 3/45** (2013.01)

Citation (search report)

See references of WO 2014095651A1

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Designated extension state (EPC)

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DOCDB simple family (publication)

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