

Title (en)

ELECTRO-OPTICAL DEVICE HAVING A LARGE PIXEL MATRIX

Title (de)

ELEKTROOPTISCHE VORRICHTUNG MIT GROSSER PIXELMATRIZ

Title (fr)

DISPOSITIF ELECTROOPTIQUE A MATRICE DE PIXELS DE GRANDE DIMENSION

Publication

EP 2997566 B1 20201230 (FR)

Application

EP 14725141 A 20140516

Priority

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- EP 2014060156 W 20140516

Abstract (en)

[origin: WO2014184373A1] At least one of the two rectangular conducting planes, P1, provided to apply a voltage at the terminals of each of the pixels of a matrix, is powered by two adjacent edges b3 and b4 from individual voltage sources Sv1 to sv6 and Sh1 to Sh6 distributed along each of the edges. The voltage sources have different voltage values, preferably but not necessarily varying in an increasing monotonic manner between a low value Vh1 and Vv1 on the side of junction J between the two edges b3 and b4 and a high value Vh6 and Vv6 on the other side of each of the edges. The two edges b3 and b4 by which the conducting plane is mainly powered are cut to form electrical contact points that are locally insulated from each other and spaced apart at regular intervals, each being powered by an individual respective source of voltage. The other conducting plane can be powered in the same way.

IPC 8 full level

G09G 3/32 (2016.01); H01L 27/32 (2006.01)

CPC (source: EP US)

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G09G 2320/0233 (2013.01 - EP US); **G09G 2330/028** (2013.01 - EP US)

Citation (examination)

- US 2010177086 A1 20100715 - NAKAMURA NORIHIRO [JP], et al
- US 2013106676 A1 20130502 - ONO SHINYA [JP], et al
- US 2011127537 A1 20110602 - MATSUMURO TOMONORI [JP]
- JP 2008046393 A 20080228 - SEIKO EPSON CORP

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DOCDB simple family (publication)

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JP 2016520872 A 20160714; JP 6486333 B2 20190320; KR 102178608 B1 20201113; KR 20160011202 A 20160129;
TW 201514954 A 20150416; TW I620164 B 20180401; US 2016086547 A1 20160324; US 9679519 B2 20170613

DOCDB simple family (application)

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