

Title (en)

METHOD FOR THE TEMPORAL CALIBRATION OF A SWITCHED CAPACITOR ARRAY

Title (de)

VERFAHREN ZUR ZEITLICHEN KALIBRIERUNG EINES GESCHALTETEN KONDENSATORARRAYS

Title (fr)

PROCÉDÉ POUR L'ÉTALONNAGE DANS LE TEMPS D'UNE MATRICE DE CONDENSATEURS CONNECTÉE

Publication

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Application

**EP 13774408 A 20131008**

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Abstract (en)

[origin: WO2015051824A1] The invention relates to a method for calibrating an analog memory array, which analog memory array has a number (n) of memory cells, which can be selectively connected to a signal input, and a control circuit, which activates the memory cells in succession and preferably cyclically in such a way that each memory cell stores a voltage value of a signal fed into the signal input with a local time difference Tloc in relation to the immediately preceding memory cell, wherein the voltage values stored in the memory cells are digitalized in succession. In the method according to the invention a local time difference Tloc is determined for each memory cell. By means of a second periodic signal fed into the signal input and stored in all memory cells, which second periodic signal has a known period T2 and has a linear slope at least in some segments, a recalibration is performed, in which a global time difference Tx,y is determined at least once between a first memory cell Sx and a second memory cell Sy that is not directly adjacent, by means of which global time difference the local time differences Tloc are then iteratively corrected.

IPC 8 full level

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