Title (en)

METHOD FOR THE TEMPORAL CALIBRATION OF A SWITCHED CAPACITOR ARRAY

Title (de)

VERFAHREN ZUR ZEITLICHEN KALIBRIERUNG EINES GESCHALTETEN KONDENSATORARRAYS

Title (fr)

PROCÉDÉ POUR L'ÉTALONNAGE DANS LE TEMPS D'UNE MATRICE DE CONDENSATEURS CONNECTÉE

Publication

EP 3055866 A1 20160817 (DE)

Application

EP 13774408 A 20131008

Priority

EP 2013070892 W 20131008

Abstract (en)

[origin: WO2015051824A1] The invention relates to a method for calibrating an analog memory array, which analog memory array has a number (n) of memory cells, which can be selectively connected to a signal input, and a control circuit, which activates the memory cells in succession and preferably cyclically in such a way that each memory cell stores a voltage value of a signal fed into the signal input with a local time difference Tloc in relation to the immediately preceding memory cell, wherein the voltage values stored in the memory cells are digitalized in succession. In the method according to the invention a local time difference Tloc is determined for each memory cell. By means of a second periodic signal fed into the signal input and stored in all memory cells, which second periodic signal has a known period T2 and and has a linear slope at least in some segments, a recalibration is performed, in which a global time difference Tx,y is determined at least once between a first memory cell Sx and a second memory cell Sy that is not directly adjacent, by means of which global time difference the local time differences Tloc are then iteratively corrected.

IPC 8 full level

G11C 29/02 (2006.01); **G11C 27/02** (2006.01)

CPC (source: EP)

G11C 27/024 (2013.01); **G11C 29/023** (2013.01)

Citation (search report)

See references of WO 2015051824A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

WO 2015051824 A1 20150416; EP 3055866 A1 20160817

DOCDB simple family (application)

EP 2013070892 W 20131008; EP 13774408 A 20131008